

## Substrate@Europe

AT&S High Performance Computing Substrates – Small Volume and R&D Prototyping Line

|                                 |                                                                 |                        |            |
|---------------------------------|-----------------------------------------------------------------|------------------------|------------|
| <b>Programm / Ausschreibung</b> | FinVn 22/23, IPCEI, IPCEI Mikorelektronik II Ausschreibung 2022 | <b>Status</b>          | laufend    |
| <b>Projektstart</b>             | 01.10.2021                                                      | <b>Projektende</b>     | 31.12.2026 |
| <b>Zeitraum</b>                 | 2021 - 2026                                                     | <b>Projektlaufzeit</b> | 63 Monate  |
| <b>Keywords</b>                 | Semiconductor Technologies, Smart Manufacturing                 |                        |            |

### Projektbeschreibung

siehe englische Beschreibung

### Abstract

Substrates@Europe is related to the IPCEI ME/CT work stream THINK – work package 2 Semiconductor Technologies and smart manufacturing and addresses

- ) Modeling the future: Virtual development & system understanding for European technology leadership
- ) Miniaturization: Features on Substrate level capable of routing 2nm node processors
- ) Flexible Manufacturing: To speed up development cycles and realize small volumes
- ) Integration and other measures to increase power efficiency on system level

### Projektpartner

- AT & S Austria Technologie & Systemtechnik Aktiengesellschaft